



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2822
Examiner: Not yet assigned
Confirmation No.: 2321

In re PATENT APPLICATION of:

Applicant(s): Motoki KOBAYASHI et al.

)

Serial No.: 10/673,499

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Filed: September 30, 2003

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For: SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR DEVICE
FABRICATION METHOD

SUBMISSION OF
FORMAL DRAWINGS

Docket No.: FUJI 137

)

April 1, 2004

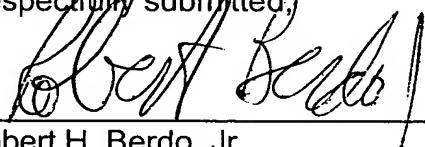
MAIL STOP PGPUB

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants hereby submit seven (7) sheets of corrected Formal Drawings (Figs. 1-10).

Respectfully submitted,



Robert H. Berdo, Jr.
Registration No. 38,075
RABIN & BERDO, PC
Customer No. 23995
Telephone: 202-371-8976
Facsimile: 202-408-0924

April 1, 2004
Date

RHB:crh

FEE ENCLOSED: \$
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